

Ok to enter
7/7/05

Aug-26-05 04:23pm From-B S T Z

310 920 5888

T-238 P.005/013 F-336

RECEIVED
CENTRAL FAX CENTER

AUG 26 2005

REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER

Our Ref. No.: 42390P5258D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Sanjay Dabral, Krishna Seshan

Application No.: 09/651,385

Filed: August 29, 2000

For: DIODE AND TRANSISTOR DESIGN
FOR HIGH SPEED I/O

Examiner: Diaz, Jose R.

Art Unit: 2815

Confirmation No.: 9681

SUPPLEMENTAL AMENDMENT AND RESPONSE

TO FINAL OFFICE ACTION

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Final Office Action dated June 7, 2005, and the Advisory Action dated August 17, 2005, Applicants respectfully request entry of the amendments set forth below and consideration of the remarks that follow.

08/29/2005 HBIZUNES 00000033 022666 09651385

01 FC:1201 200.00 DA